



ADRIAN LITA

PROFILE

Well organized, creative, and proven electronics engineer and software developer/architect. Experience working with multicultural teams, fluent in Romanian, English, with strong knowledge of German and French.

CONTACT

PHONE:
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GITHUB:
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SKILLS



WORK EXPERIENCE

SENIOR SYSTEM ENGINEER

VARTOS SRL, Bucharest, Romania

OCT 2021 – PRESENT

Building high efficiency functionality into the Vartos OS standard library.

SENIOR TELECOM ENGINEER

highstreet technologies, Berlin, Germany

JUL 2020 – PRESENT

NETCONF/YANG applications using C/C++, libyang/sysrepo, Docker

CEO / SOFTWARE ENGINEER

AXIPlus Engineering, Bucharest, Romania

MAR 2015 – PRESENT

Design and development of custom outsourced products, as well as in-house projects, such as the eTitan RTOS.

SENIOR II APPLICATIONS ENGINEER

Microchip Technology, Bucharest, Romania

SEP 2011 – NOV 2013 | NOV 2017 – JUN 2021

Actively developing applications and specifications for current sensors (PAC19xx family); lead a team of 3 application engineers on ADCs, DACs and Digipots.

RESEARCH AND TEACHING ASSISTANT

POLITEHNICA University of Bucharest, Romania

FEB 2014 – OCT 2019

Taught programming courses (C, C++, Image Processing and Optimizations) to over 1000 students throughout more than 6 years.

For the remaining items, please see complete CV on my webpage or LinkedIn.

EDUCATION

MASTER OF SCIENCE / AUTOMOTIVE ENGINEERING,
POLITEHNICA UNIVERSITY OF BUCHAREST, ROMANIA
2012–2014

BACHELOR OF SCIENCE / APPLIED ELECTRONICS
POLITEHNICA UNIVERSITY OF BUCHAREST, ROMANIA
2008–2012